

09/980509

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FORM 1449A/PTO
PTO/SB/08A (08/00)U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Application No.	New
Filing Date	Herewith
First Named Inventor	McCORD, Don
Group Art Unit	Unknown
Examiner Name	Unknown

Sheet	1	of	1	Attorney Docket No.	37802PCT-US
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U.S. PATENT DOCUMENTS

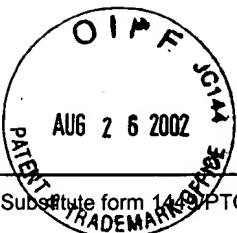
Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Office	Number	Kind Code (if known)				
POA	AA	WO	01/35110	A1	McCo:rd, Don	05.17.2001		
POA	AB	WO	00/13186	A1	Tanisys Technology, Inc.	03.09.2000		

OTHER PRIOR ART

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.		
POA	AC	GASBARRO, J.A.; HOROWITZ, M.A.; RAMBUS INC.; <i>Techniques for Characterizing Drains with a 500 MHZ Interface</i> , PROCEEDINGS OF THE INTERNATIONAL TEST CONFERENCE, US, New York, IEEE, 2 October 1994 (1994-10-02), pages 516-525		
POA	AD	GOLDBERG, J.M.; <i>Timing: The Key to Rambus Testing</i> , TEST AND MEASUREMENT WORLD, US, Cahners Publishing, Denver, Vol. 17, No. 11, 1 October 1997, pages 53-54, 56, 58-29		
POA	AE	HO, C.C.; <i>Defining Tomorrow's Memory Module Tester</i> , E E: EVALUATION ENGINEERING, Vol. 38, No. 3, March 1999, pages 14-18		
Examiner Signature			Date Considered	10/7/03



Substitute form 1A-PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number 09/980,509

Filing Date October 19, 2001

First Named Inventor Don McCord

Group Art Unit 2836

Examiner Name Unknown

Sheet

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of

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Attorney Docket Number 37802PCT-US

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Examiner Initials	IDS Doc #	U.S. Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY
POV	AA	5,325,053	Gasbarro et al.	06-28-1994

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POV	BA	WO	01/63311 A2	McCord, Don	08-30-2001	
POV	BB	WO	01/63311 A3	McCord, Don	08-30-2001	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	IDS Doc #	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	English Translation
POV	CA	POINTL, Peter; <i>Interfacing, Often a Performance Bottleneck Between Ate and Device Under Test</i> , Computer Society Press, 1st European Test Conference, April 1989, pages 94-99	

Examiner Signature		Date Considered	10/7/03
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